

INDIAN INSTITUTE OF TECHNOLOGY ROPAR
Department of Electrical Engineering
Rupnagar, Punjab - 140001

Advertisement No.: IRIS/EE/01

Dated: 9th July 2026

Applications from Indian nationals are invited for a Project Appointment under the following project. The appointment shall be on a contractual basis for a strict duration of ONE (01) year only, subject to periodic performance reviews. The Principal Investigator (Supervisor) reserves the right to terminate the contract at any time if the candidate's performance is found unsatisfactory.

Item	Project Details
Title of the Project	DESIGN & DEVELOPMENT OF FAULT TOLERANT ALGORITHMS FOR COTS FPGAs
Funding Agency	Institute Research Initiation Support (IRIS) grant, IIT Ropar
Name of the Project Investigator (PI)	Dr. Abhishek Kumar Singh Email ID: akumar@iitrpr.ac.in
Duration of the Project	1 year from date of Joining

Position, Fellowship, and Eligibility Criteria: -

- **Post(s):** Junior Research Fellow (JRF) – **01 Position**
- **Consolidated Fellowship:** Rs. 37,000/- p.m. plus 9% HRA in case of no accommodation on campus. Suitable on-campus accommodation may be provided, subject to availability.
- **Essential Qualifications:**
 - Postgraduate degree (M. Tech/M.E./M.S.) in **Electrical Engineering / Electronics & Communication Engineering / Instrumentation/Computer Science & Engineering** or equivalent with at least 60% marks (or equivalent CGPA) in the qualifying degree.
 - **OR** Bachelor's degree (minimum 4 years in person) in **Electrical Engineering / Electronics & Communication Engineering/ Computer Science & Engineering** or equivalent with at least 70% marks (or equivalent CGPA) in the qualifying degree.
 - **Mandatory Requirement:** Qualified National Eligibility Test **GATE/UGC-CSIR NET-JRF**.
- **Desirable Skills:** Proficiency in VHDL/Verilog, hands-on experience with FPGA development tools (e.g., Vivado), and a foundational understanding of digital design and fault-tolerant architectures
- **SC/ST/Women/PwD candidates are highly encouraged to apply. A 5% relaxation of marks in the qualifying degree may be granted to SC/ST/PwD candidates as per guidelines.**

How to Apply: -

Interested candidates must submit a single PDF file containing the following documents:

1. Detailed Curriculum Vitae (CV) with a recent passport-size photograph embedded.
 2. Supporting documents (scanned copies of academic transcripts/certificates).
 3. Proof of National Level Examination qualification (GATE/NET certificate).
- **Submission Email:** Send the single PDF via email to akumar@iitrpr.ac.in
 - **Subject Line:** Application for JRF – IRIS/EE
 - **Application Deadline:** On or before 15th July 2026.
 - **Strict Policy:** No request or application will be entertained after the due date for any reason whatsoever.

Selection Process & Interview: -

- **Mode of Interview:** Online Mode (The interview link will be shared only with shortlisted candidates).
- **Date of Interview:** 16th July or 17th July 2026.
- Shortlisted candidates will be notified via email regarding their exact interview time slot. Candidates are advised to monitor their email/Spam closely on the 16th July 2026
- The Institute/Selection Committee reserves the right to fix higher criteria for shortlisting, and **the right to fill or not fill the position** advertised if no suitable candidate is found.